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RULE				

APPLICANTS

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** CONTINUING DATA *None NL*

** FOREIGN APPLICATIONS *Yes NL*

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Foreign Priority claimed <input checked="" type="checkbox"/> yes <input type="checkbox"/> no	STATE OR COUNTRY JAPAN	SHEETS DRAWING 8	TOTAL CLAIMS 16	INDEPENDENT CLAIMS 2
35 USC 119 (a-d) conditions met <input checked="" type="checkbox"/> yes <input type="checkbox"/> no Met after Allowance <i>NL</i>	Verified and Acknowledged <i>None</i>	Examiner's Signature <i>None</i>	Initials <i>NL</i>	

ADDRESS

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TITLE

Clock delay adjusting method of semiconductor integrated circuit device and semiconductor integrated circuit device formed by the method

FILING FEE

FEES: Authority has been given in Paper
No. _____ to charge/credit DEPOSIT ACCOUNT

☐ All Fees☐ 1.16 Fees (Filing)☐ 1.17 Fees (Processing Ext. of time)